

# INFORMATION DISCLOSURE CITATION IN AN APPLICATION

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 APPLICANT  
Yasuhiko NOMURA, et al.

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## U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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## FOREIGN PATENT DOCUMENTS

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						Yes No
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## OTHER ARTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
MG		Nishinaga Tatsu, "Hetero-epitaxy with Large Lattice Mismatch and Microchannel Epitaxy of Compound Semiconductor." Department of Electronic Engineering, Graduate School of Engineering, Volume 21, No. 6, pp. 320-325, 2000
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EXAMINER

DATE CONSIDERED

Maria Golub

08/17/2005

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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